



Product Description: T420HW04 V1 TFT-LCD PANEL			
AUO Model Name: T420HW04 V1			
Customer Part No/Project Name:			
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Product Functional Specification

42" Full-HD Color TFT-LCD Module Model Name: T420HW04 V1

(*) Preliminary Specification
() Final Specification

Note : This specification is subject to change without notice.



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Record of Revision

[illegible]



1. General Description

This specification applies to the 42 inch Color TFT-LCD Module T420HW04 V1. This LCD module has a TFT active matrix type liquid crystal panel 1920x1080 pixels, and diagonal size of 42 inch. This module supports 1920x1080 Full-HD mode (Non-interlace).

Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal and Frame Rate Control (FRC) for each dot.

The T420HW04 V1 has been designed to apply the 10bits 4 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important.

T420HW04 V1 Backlight unit used C-balance board (inverter-less) solution. This backlight unit should bundle integral TV power system to use.

* General Information

Items	Specification	Unit	Note
Active Screen Size	42.02	inches	
Display Area	930.24(H) x 523.26(V)	mm	
Outline Dimension	983.0(H) x 576.0(V) x 44.1(D)	mm	Without inverter
Driver Element	a-Si TFT active matrix		
Display Colors	1073.7M	Colors	
Color Gamut	72	%	NTSC
Number of Pixels	1920 x 1080	Pixel	
Pixel Pitch	0.4845	mm	
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		
Lamp quantity, type	16pcs, Straight type	pcs	
Surface Treatment	Anti-Glare coating (Haze 11%) Hard coating (3H)		



2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Item	Symbol	Min	Max	Unit	Note
Power Supply Input Voltage	VDD	-0.3	14	[Volt]	1
Logic Input Voltage	Vin	-0.3	3.6	[Volt]	1
Ambient Operating Temperature	T _{OP}	0	+50	[°C]	2
Ambient Operating Humidity	H _{OP}	10	80	[%RH]	2
Storage Temperature	T _{ST}	-20	+60	[°C]	2
Storage Humidity	H _{ST}	10	80	[%RH]	2
Shock (non-operation)		-	50	G	3
Vibration (non-operation)		-	1.5	G	4
Thermal shock		-20	60	C	5
Panel surface temp			60	C	6

Note 1 : Duration = 50msec

Note 2 : Maximum Wet-Bulb should be 50°C and No condensation.

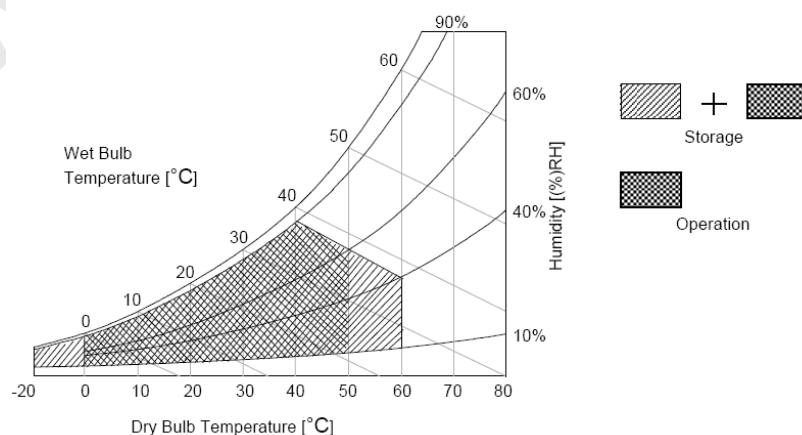
Note 3 : Half sine wave, shock level : 50G(11ms), direction : $\pm x$, $\pm y$, $\pm z$ (one time each direction)

Note 4 : Wave form : Random, vibration level : 1.5G RMS, Bandwidth : 10~500Hz

Duration : X,Y,Z 30min (one time each direction)

Note 5 : -20C/1hr ~ 60C/1hr, 100 cycles

Note 6 :Panel only (without TV set), Ambient temp 25C





3. Electrical Specification

The T420HW04 V1 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input, which powers the CCFL, is typically generated by an power system.

3-1 Electrical Characteristics

Parameter		Symbol	Values			Unit	Notes
			Min	Typ	Max		
LCD:							
Power Supply Input Voltage		Vdd	10.8	12	13.2	Vdc	
Power Supply Input Current		Idd	-	0.6	1.1	A	1
Power Consumption		Pc	-	7.2		Watt	1
Inrush Current		I _{RUSH}	-	-	4	A	4
LVDS Interface	Differential Input High Threshold Voltage	V _{TH}			+100	mV	3
	Differential Input Low Threshold Voltage	V _{TL}	-100			mV	3
	Common Input Voltage	V _{CIM}	0.6	1.2	1.8	V	
CMOS Interface	Input High Threshold Voltage	V _{IH} (High)	2.0		3.3	Vdc	
	Input Low Threshold Voltage	V _{IL} (Low)	0		0.8	Vdc	
Life Time			-	50000		Hours	2

The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the Integrated Power Board (IPB). All the parameters of an IPB should be carefully designed so as not to produce too much leakage current from high-voltage output of the IPB. When you design or order the IPB, please make sure unwanted lighting caused by the mismatch of the lamp and the IPB (no lighting, flicker, etc) never occurs. When you confirm it, the LCD Assembly should be operated in the same condition as installed in your instrument.

Do not attach a conducting tape to lamp connecting wire. If the lamp wire attach to conducting tape, TFT-LCD Module have a low luminance and the inverter has abnormal action because



leakage current occurs between lamp wire and conducting tape.

The relative humidity must not exceed 80% non-condensing at temperatures of 40°C or less. At temperatures greater than 40°C, the wet bulb temperature must not exceed 39°C. When operate at low temperatures, the brightness of CCFL will drop and the lifetime of CCFL will be reduced.

Note :

1. $V_{dd}=12.0V$, $f_v=120Hz$, $f_{CLK}=80\text{ Mhz}$, $25^{\circ}C$, V_{dd} Duration time= $400\mu s$, Test pattern : white pattern
2. The life is determined as the time at which luminance of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at $25\pm 2^{\circ}C$.
3. $V_{CIM} = 1.2V$

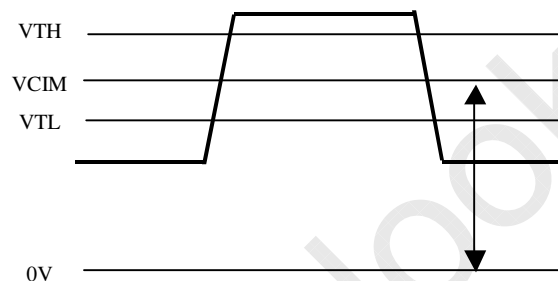
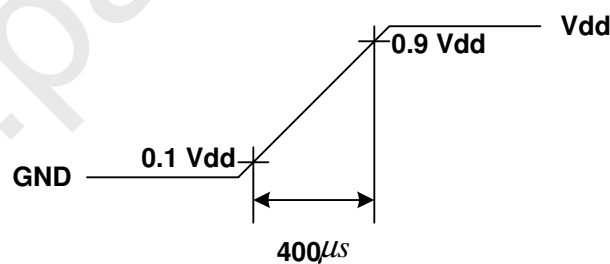


Figure : LVDS Differential Voltage

4. Measurement Condition: Rising time = $400\mu s$





3-2 Interface Connections

- LCD connector 1 : P-TWO 187059-5122 which is compatible FI-RE51S-HF (JAE)

No	Symbol	Description	No	Symbol	Description
1	NC	No connection	27	NC or GND	No Connect or Ground
2	NC	No connection	28	R2_0-	LVDS Channel 2, Signal 0-
3	NC	No connection	29	R2_0+	LVDS Channel 2, Signal 0+
4	NC	No connection	30	R2_1-	LVDS Channel 2, Signal 1-
5	NC	No connection	31	R2_1+	LVDS Channel 2, Signal 1+
6	NC	No connection	32	R2_2-	LVDS Channel 2, Signal 2-
7	LVDS SEL	LVDS order, Low /Open for NS ,High for JEIDA	33	R2_2+	LVDS Channel 2, Signal 2+
8	NC	No connection	34	GND	Ground
9	NC	No connection	35	R2_CLK-	LVDS Channel 2, Clock -
10	NC	No connection	36	R2_CLK+	LVDS Channel 2, Clock +
11	GND	Ground	37	GND	Ground
12	R1_0-	LVDS Channel 1, Signal 0-	38	R2_3-	LVDS Channel 2, Signal 3-
13	R1_0+	LVDS Channel 1, Signal 0+	39	R2_3+	LVDS Channel 2, Signal 3+
14	R1_1-	LVDS Channel 1, Signal 1-	40	R2_4-	LVDS Channel 2, Signal 4-
15	R1_1+	LVDS Channel 1, Signal 1+	41	R2_4+	LVDS Channel 2, Signal 4+
16	R1_2-	LVDS Channel 1, Signal 2-	42	NC or GND	No Connect or Ground
17	R1_2+	LVDS Channel 1, Signal 2+	43	NC or GND	No Connect or Ground
18	GND	Ground	44	GND	Ground
19	R1_CLK-	LVDS Channel 1, Clock -	45	GND	Ground
20	R1_CLK+	LVDS Channel 1, Clock +	46	GND	Ground
21	GND	Ground	47	V _{LCD}	Operating Voltage supply, +12V DC regulated
22	R1_3-	LVDS Channel 1, Signal 3-	48	V _{LCD}	Operating Voltage supply, +12V DC regulated
23	R1_3+	LVDS Channel 1, Signal 3+	49	V _{LCD}	Operating Voltage supply, +12V DC regulated
24	R1_4-	LVDS Channel 1, Signal 4-	50	V _{LCD}	Operating Voltage supply, +12V DC regulated
25	R1_4+	LVDS Channel 1, Signal 4+	51	V _{LCD}	Operating Voltage supply, +12V DC regulated
26	NC or GND	No Connect or Ground	-	-	-

Note: 1. All GND (ground) pin should be connected together to the LCD module's metal frame.

2. All V_{LCD} (power input) pins should be connected.



- LCD connector 2 : P-TWO 187060-4122 which is compatible FI-RE41S-HF (JAE)

No	Symbol	Description	No	Symbol	Description
1	NC	No connection	27	R4_0+	LVDS Channel 4, Signal 0+
2	NC	No connection	28	R4_1-	LVDS Channel 4, Signal 1-
3	NC	No connection	29	R4_1+	LVDS Channel 4, Signal 1+
4	NC	No connection	30	R4_2-	LVDS Channel 4, Signal 2-
5	NC	No connection	31	R4_2+	LVDS Channel 4, Signal 2+
6	NC	No connection	32	GND	Ground
7	NC	No connection	33	R4_CLK-	LVDS Channel 4, Clock -
8	NC	No connection	34	R4_CLK+	LVDS Channel 4, Clock +
9	GND	Ground	35	GND	Ground
10	R3_0-	LVDS Channel 3, Signal 0-	36	R4_3-	LVDS Channel 4, Signal 3-
11	R3_0+	LVDS Channel 3, Signal 0+	37	R4_3+	LVDS Channel 4, Signal 3+
12	R3_1-	LVDS Channel 3, Signal 1-	38	R4_4-	LVDS Channel 4, Signal 4-
13	R3_1+	LVDS Channel 3, Signal 1+	39	R4_4+	LVDS Channel 4, Signal 4+
14	R3_2-	LVDS Channel 3, Signal 2-	40	NC or GND	No Connect or Ground
15	R3_2+	LVDS Channel 3, Signal 2+	41	NC or GND	No Connect or Ground
16	GND	Ground			
17	R3_CLK-	LVDS Channel 3, Clock -			
18	R3_CLK+	LVDS Channel 3, Clock +			
19	GND	Ground			
20	R3_3-	LVDS Channel 3, Signal 3-			
21	R3_3+	LVDS Channel 3, Signal 3+			
22	R3_4-	LVDS Channel 3, Signal 4-			
23	R3_4+	LVDS Channel 3, Signal 4+			
24	NC or GND	No Connect or Ground			
25	NC or GND	No Connect or Ground			
26	R4_0-	LVDS Channel 4, Signal 0-			

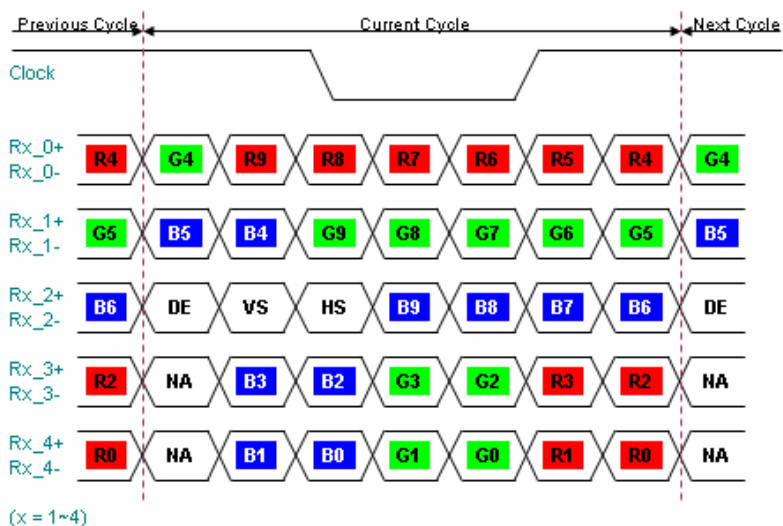


4 CH LVDS data mapping



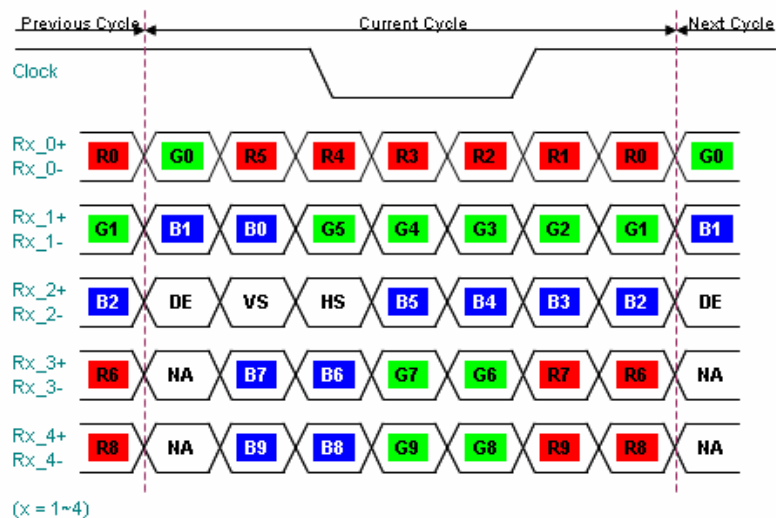
LVDS Option = High → JEIDA

JEIDA



LVDS Option = Low/Open → NS

NS





Backlight Connector Pin Configuration

Electrical specification

	Description		Min	Typ	Max	Unit	Condition
1	BL Operating Voltage	VBL	2043	2243	2443	Vrms	1. BL two side operating voltage at dimming ratio 100% 2. Calculation method: (notes 1) 3. fo=58KHz, IBL= 144mArms
2	BL Operating Current	IBL		144		mArms	1. BL two side operating current at dimming ratio 100%
3	Starting Voltage	Vs	1964	2164	-	Vrms	1. BL two side striking voltage. 2. Measurement by disconnect IPB and BL
			1872	2072	-		
4	Operating frequency	fo	56	58	60	kHz	
5	Striking time	St	1000	1500	2000	msec	
6	Power Consumption	PBL	162	170	178	Watt	
7	PWM Operating Frequency	F_PWM	140	-	240	Hz	95~140Hz might cause waterfall noise but not influence panel function
8	PWM Dimming Duty ratio	D_PWM	10	-	100	%	1. luminance is from 20% to 100% 2. note 2
9	Lamp type	Straight type					
10	Number of lamps	16				pcs	
11	Type of current balance	Capacitor					
12	C ballast	Cb	14.25	15	15.75	pF	

(Ta=25±5℃, Turn on for 45minutes)

2. Lamp specification (Recommendation)

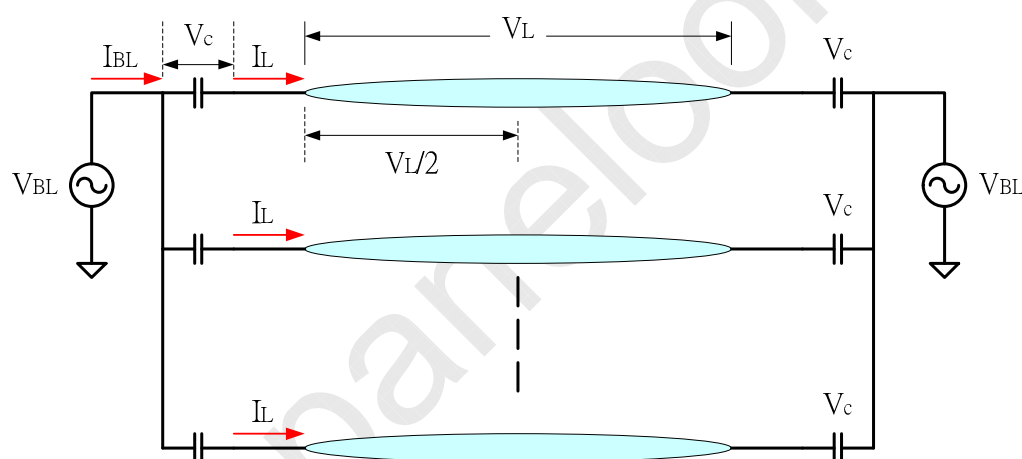
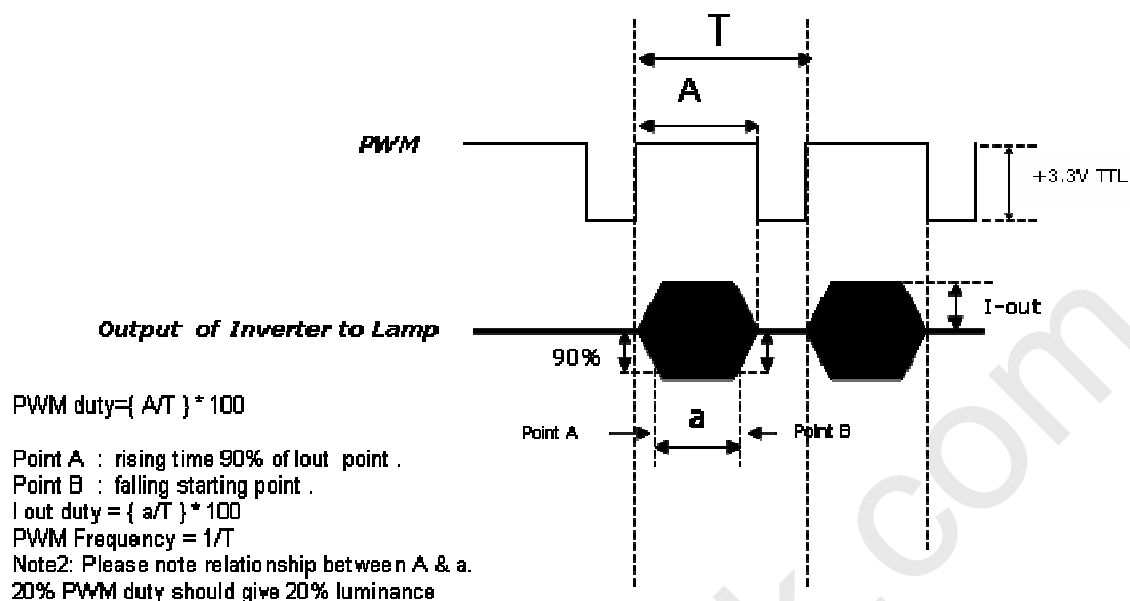
	Description		Min	Typ	Max	Unit
1	Lamp Voltage	Vlamp		1192		Vrms
2	Lamp Current	Ilamp	-	9		mArms
3	Lamp frequency	flamp	40	-	80	KHz
4	Starting Voltage	0℃		-	2140	Vrms
		25℃			1780	Vrms
5	Striking time	St	1000	-	-	msec
6	Discharge Stabilization Time		-	-	3	Min
7	Life time		50K	-	-	hr

Notes 1:

$$V_{BL} = \sqrt{\left(\frac{V_L}{2}\right)^2 + (V_C)^2}$$



Notes 2:



3. Pin assignment, connector drawing and connection configuration

CN1: B03B PASK-1 (JST) or equivalent

PIN #	Symbol	Description
1	High	I/P board high voltage supply
2	N.C.	No connection
3	High	I/P board high voltage supply

CN2: B03B PASK-1 (JST) or equivalent

PIN #	Symbol	Description
1	High	I/P board high voltage supply
2	N.C.	No connection
3	High	I/P board high voltage supply



One step solution for LCD / PDP / OLED panel application: Datasheet, inventory and accessory! www.panelook.com



3-3 Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Timing Table (DE only Mode)

Vertical Frequency Range

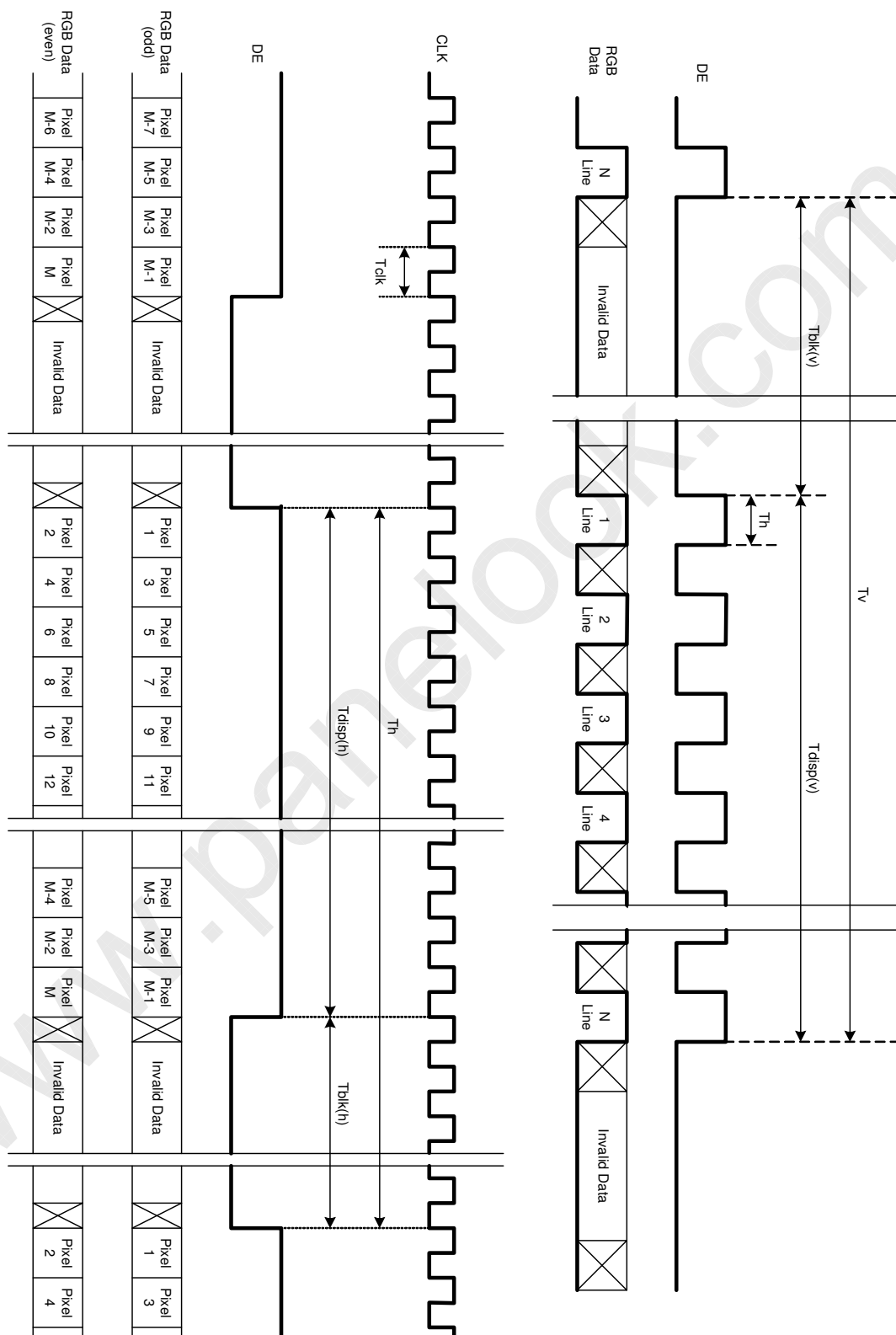
Signal	Item	Symbol	Min	Type	Max	Unit
Vertical Section	Period	Tv	1090	1130	1160	Th
	Active	Tdisp (v)	1080			Th
	Blanking	Tblk (v)	10	50	80	Th
Horizontal Section	Period	Th	532	560	580	Tclk
	Active	Tdisp (h)	480			Tclk
	Blanking	Tblk (h)	52	80	100	Tclk
Clock	Period	CLK		13.17		ns
	Frequency	Freq	69.586	75.936	80.74	MHz
Vertical Frequency	Frequency	Vs	120			Hz
Horizontal Frequency	Frequency	Hs	130.8	135.6	139.2	KHz

Vertical Frequency Range B (100Hz)

Signal	Item	Symbol	Min	Type	Max	Unit
Vertical Section	Period	Tv	1200	1280	1392	Th
	Active	Tdisp (v)	1080			Th
	Blanking	Tblk (v)	120	200	312	Th
Horizontal Section	Period	Th	550	560	580	Tclk
	Active	Tdisp (h)	480			Tclk
	Blanking	Tblk (h)	70	80	100	Tclk
Clock	Period	CLK		13.95		ns
	Frequency	Freq	66	71.68	80.736	MHz
Vertical Frequency	Frequency	Vs	100			Hz
Horizontal Frequency	Frequency	Hs	120	128	139.2	KHz



3-4 Signal Timing Waveforms





3-5 Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 8 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

COLOR DATA REFERENCE

Color		Input Color Data																															
		RED										GREEN										BLUE											
		MSB										MSB										MSB											
		R9	R8	R7	R6	R5	R4	R3	R2	R1	R0	G9	G8	G7	G6	G5	G4	G3	G2	G1	G0	B9	B8	B7	B6	B5	B4	B3	B2	B1	B0		
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	Red(1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	Green(1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0		
	Blue(1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1		
	Cyan	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1		
	Magenta	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1		
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0		
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1		
RED	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	RED(001)	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		

	RED(1022)	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	RED(1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
GREEN	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0		

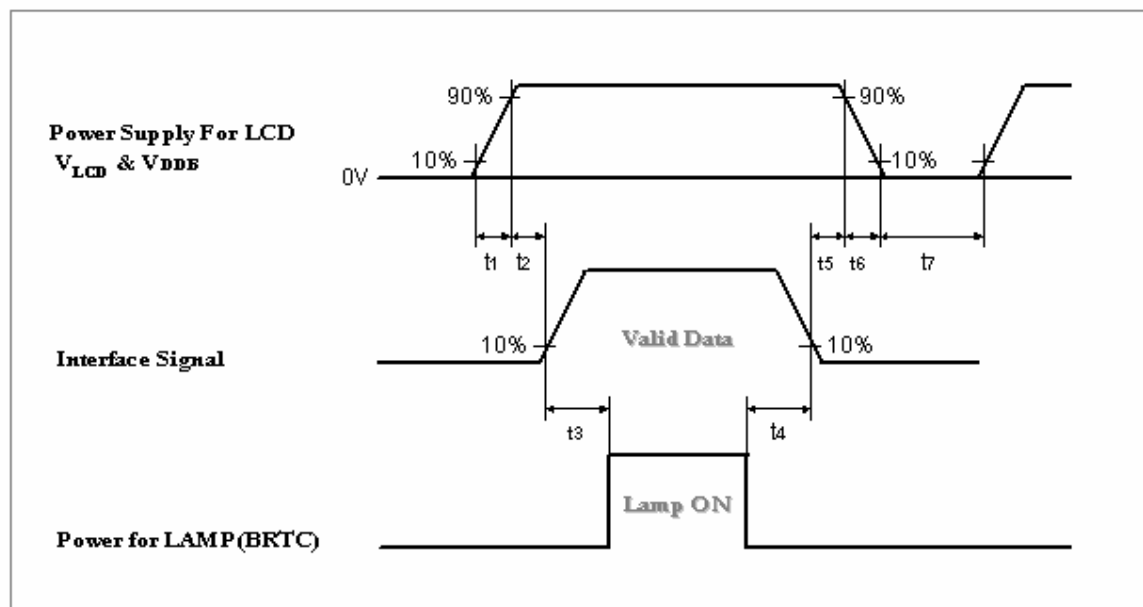
	GREEN(1022)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0		
	GREEN(1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0		
BLUE	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		

	BLUE(1022)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0		
	BLUE(1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1		



3-6 Power Sequence

1. Power sequence of panel



Parameter	Values			Units
	Min.	Typ.	Max.	
t1	0.4	-	30	ms
t2	0.1	-	50	ms
t3	300	-	-	ms
t4	10	-	-	ms
t5	0.1	-	50	ms
t6		-	300	ms
t7	500	-	-	ms

Apply the lamp voltage within the LCD operating range. When the backlight turns on before the LCD operation or the LCD turns off before the backlight turns off, the display may momentarily become abnormal.

Caution : The above on/off sequence should be applied to avoid abnormal function in the display. In case of handling, make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.



4.Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 60 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0°.

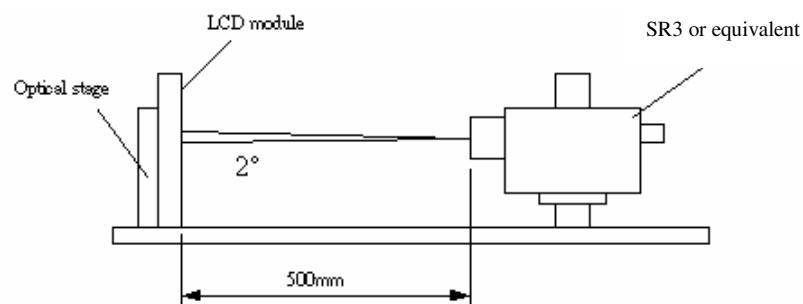


Fig.4-1 Optical measurement equipment and method

Parameter		Symbol	Values			Units	Notes
			Min.	Typ.	Max.		
Contrast Ratio		CR	3200	4000			1
Surface Luminance, white		LWH	400	500		cd/m ²	2
Luminance Variation		δ_{WHITE} 5p			1.3		3
Response Time (Average)		T_{γ}		5.5		ms	4,5 (Gray to Gray)
Color Coordinates							
	RED	R_x	Typ.-0.03	0.640	Typ.+0.03		
		R_y		0.330			
	GREEN	G_x		0.290			
		G_y		0.600			
	BLUE	B_x		0.150			
		B_y		0.060			
	WHITE	W_x		0.280			
		W_y		0.290			
Viewing Angle							Contrast Ratio>10
	x axis, right($\varphi=0^\circ$)	θ_r		89		Degree	6
	x axis, left($\varphi=180^\circ$)	θ_l		89			
	y axis, up($\varphi=90^\circ$)	θ_u		89			
	y axis, down ($\varphi=0^\circ$)	θ_d		89			



Note:

1. Contrast Ratio (CR) is defined mathematically as:

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the "white" state}}{\text{Brightness on the "black" state}}$$

2. Surface luminance is luminance value at point 1 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see Fig. 4-2. When $V_{DDB} = 24V$, $I_{DDB} = 6.4A$. $L_{WH} = L_{on1}$, Where L_{on1} is the luminance with all pixels displaying white at center 1 location.

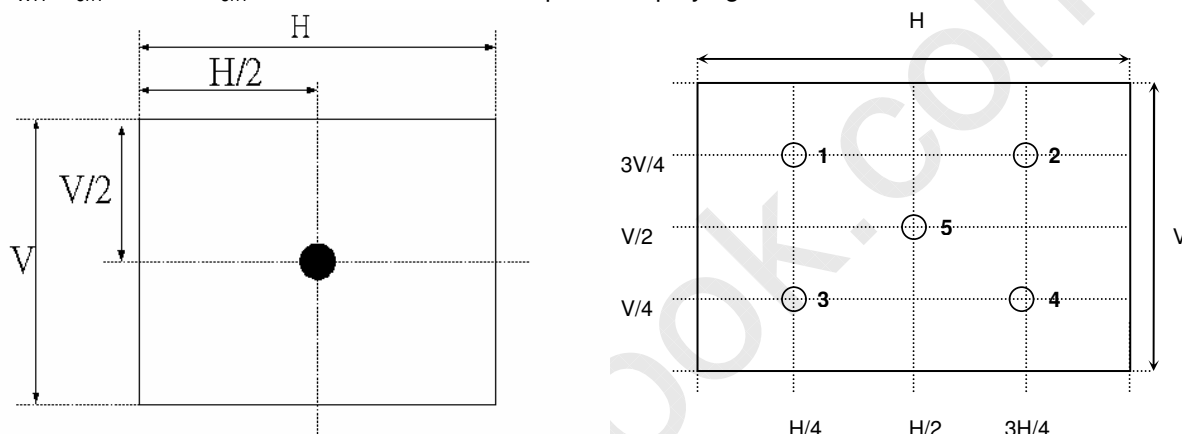


Fig.4-2 Optical measurement point

3. The variation in surface luminance, δ_{WHITE} is defined under 100% brightness as:

$$\delta_{\text{WHITE(5P)}} = \frac{\text{Maximum}(L_{on1}, L_{on2}, \dots, L_{on5})}{\text{Minimum}(L_{on1}, L_{on2}, \dots, L_{on5})}$$



4. Response Time:

(a) Tr = full black to full white, 10%~90%

(b) Tf = full white to full black, 90%~10%

(c) G-to-G: average response time among brightness of 0%, 25%, 50%, 75% & 100%.

	0%	25%	50%	75%	100%
0%		tr: 0%→25%	tr: 0%→50%	tr: 0%→75%	tr: 0%→100%
25%	tf: 25%→0%		tr: 25%→50%	tr: 25%→75%	tr: 25%→100%
50%	tf: 50%→0%	tf: 50%→25%		tr: 50%→75%	tr: 50%→100%
75%	tf: 75%→0%	tf: 75%→25%	tf: 75%→50%		tr: 75%→100%
100%	tf: 100%→0%	tf: 100%→25%	tf: 100%→50%	tf: 100%→75%	

5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see Fig. 4-3. (Optical measurement by SR3)

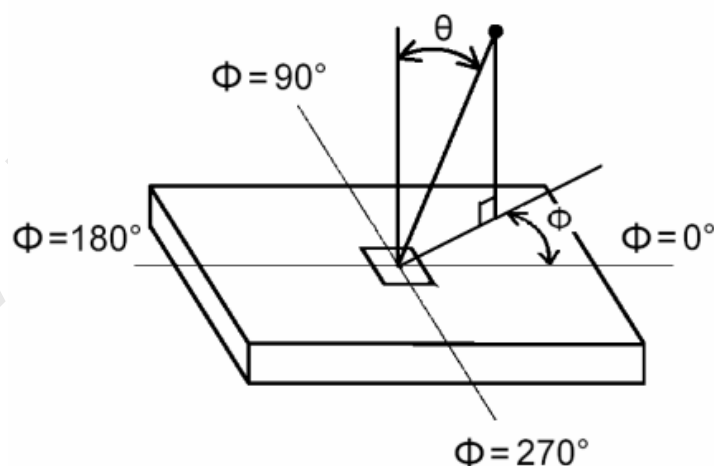


Fig.4-3 Viewing Angle Definition



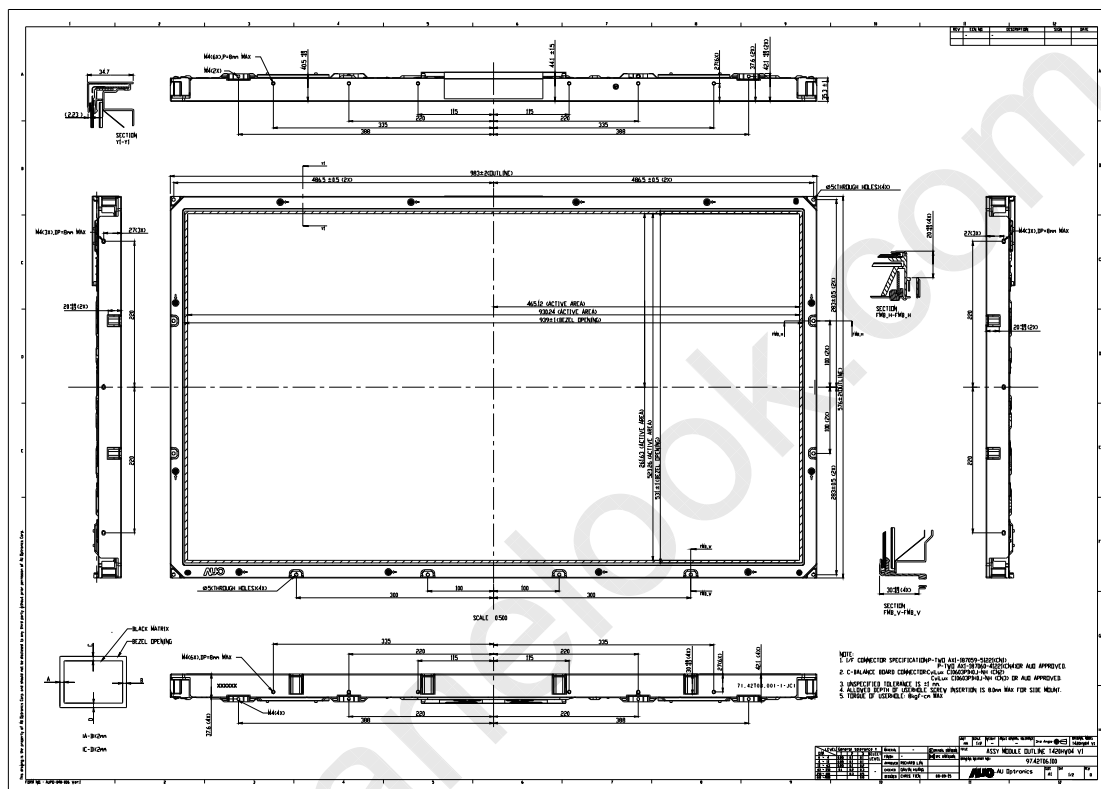
5.Mechanical Characteristics

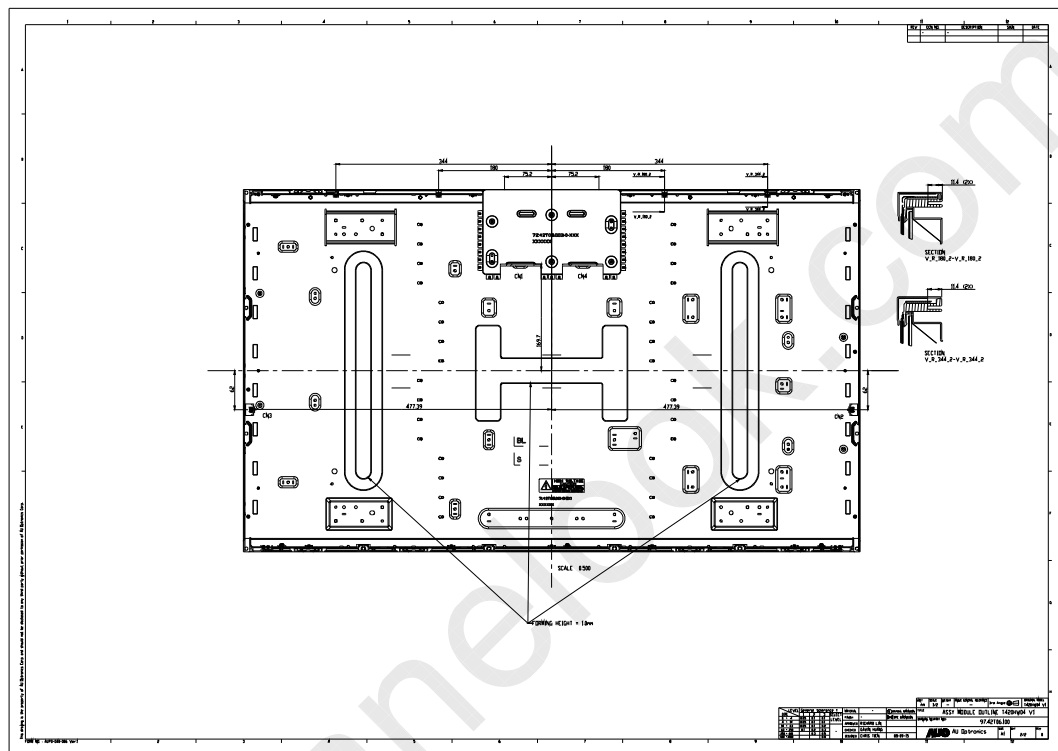
The contents provide general mechanical characteristics for the model T420HW04. In addition the figures in the next page are detailed mechanical drawing of the LCD.

Outline Dimension	Horizontal (typ.)	983.0mm
	Vertical (typ.)	576.0mm
	Depth (typ.)	44.1mm (without inverter)
Bezel Area	Horizontal (typ.)	939.0mm
	Vertical (typ.)	531.26mm
Active Display Area	Horizontal	930.24mm
	Vertical	523.26mm
Weight	12600g (typ.)	
Surface Treatment	AG, 3H	



2D drawing







6. Reliability

Panel condition in RA test

Brightness: 500nits

No	Test Item	Condition
1	High temperature storage test	Ta=60℃ 300h
2	Low temperature storage test	Ta= -20℃ 300h
3	High temperature operation test	Ta=50℃ 300h
4	Low temperature operation test	Ta=-5℃ 300h
5	Vibration test (non-operating)	Wave form: random Vibration level: 1.5G RMS Bandwidth: 10-300Hz, Duration: X, Y, Z 30min One time each direction
6	Shock test (non-operating)	Shock level: 50G Waveform: half sine wave, 11ms Direction: ±X, ±Y, ±Z One time each direction
7	Vibration test (with carton)	Wave form: random Vibration level: 1.5G RMS Bandwidth: 10-200Hz, Duration: X, Y, Z 30min One time each direction
8	Drop test (with carton)	Height: 25.4cm 1 corner, 3 edges, 6 surfaces (ASTMD4169-I)

Result Evaluation Criteria

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.



7. International Standard

7-1. Safety

- (1) UL60065, Underwriters Laboratories, Inc. (AUO file number : E204356)
Standard for Safety of Information Technology Equipment Including electrical Business Equipment.
- (2) CSA E60065, Canadian Standards Association
Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.
- (3) IEC 60065 ver. 7th, European Committee for Electro technical Standardization (CENELEC)
EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

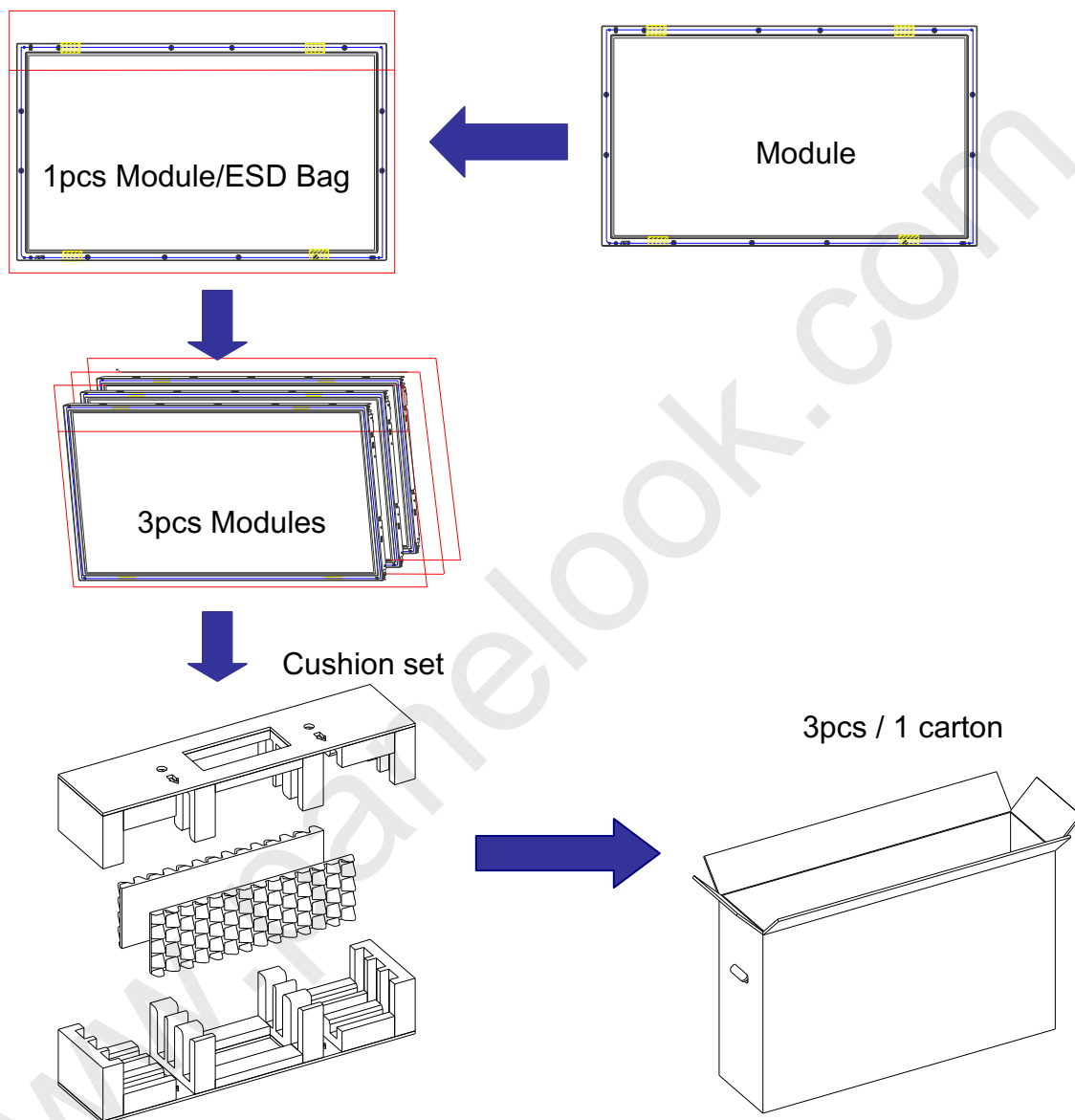
7-2. EMC

- (1) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National standards Institute(ANSI), 1992
- (2) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special committee on Radio Interference.
- (3) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electro technical Standardization. (CENELEC), 1998



8.Packing

8-1 Packing Instruction



Package information:

Carton outside dimension : 1087x285x716mm

Carton/Package weight : 3kg